

Lemma: if  $e = \underline{H}e^T = \underline{s}^T$  then  $e' = e + s = \underline{H}e^T + \underline{s}^T = \underline{s}^T + \underline{H}e^T = \underline{H}(e + s)^T = \underline{H}e'^T = \underline{s}^T$

$$C(5,2) \quad \underline{G}_{2 \times 5} = \begin{pmatrix} 1 & 0 & 1 & 1 & 0 \\ 0 & 1 & 1 & 1 & 1 \end{pmatrix} \Rightarrow \underline{H}_{3 \times 5} = \begin{pmatrix} 1 & 1 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 \end{pmatrix}$$

$$\underline{s} = (0 \ 0 \ 1)$$

$$E_s = \{(0 \ 0 \ 0 \ 0 \ 1), (0 \ 1 \ 1 \ 1 \ 0), (1 \ 0 \ 1 \ 1 \ 1), (1 \ 1 \ 0 \ 0 \ 0)\}$$

$$\begin{pmatrix} 1 & 1 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 1 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \\ 1 \end{pmatrix}$$

$$\underline{e}^{(0)} = (0 \ 0 \ 0 \ 0 \ 0), \underline{e}^{(1)} = (0 \ 1 \ 1 \ 1 \ 1), \underline{e}^{(2)} = (1 \ 0 \ 1 \ 1 \ 0), \underline{e}^{(3)} = (1 \ 1 \ 0 \ 0 \ 1)$$

Hamming codes - Constructing a code which can correct every single error  
 → we need good channel in which the possibility of double errors are insignificant,  
 so that, single error detection is sufficient → wired MODEM connection

$$\underline{e}^{(i)} = (\dots, 0, 0, 1, 0, 0, \dots) \xrightarrow{??} "i"$$

↑  
"i"-th bit

$$\underline{H}\underline{s}^T = \underline{s} \leftarrow \text{computable} \Rightarrow \underline{H}(\underline{s} + \underline{e}^{(i)})^T = \underline{s}^T \Rightarrow \underbrace{\underline{H}\underline{s}^T}_{\underline{0}^T} + \underline{H}\underline{e}^{(i)T} = \underline{s}^T \Rightarrow \underline{H}\underline{e}^{(i)T} = \underline{s}^T$$

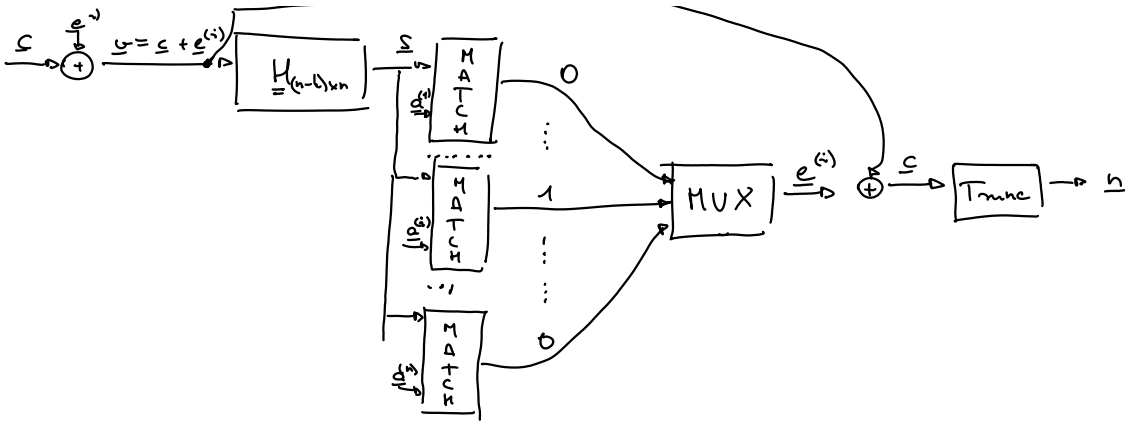
↑  
sicher beobachtbar

$$\left( \begin{array}{c|c|c|c|c} \hline \underline{q}^{(1)T} & \underline{q}^{(2)T} & \dots & \underline{q}^{(i)T} & \dots & \underline{q}^{(n)T} \\ \hline \end{array} \right) \cdot \begin{pmatrix} 0 \\ 0 \\ \dots \\ 0 \\ \dots \\ 0 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \\ \dots \\ \underline{q}^{(i)} \\ \dots \\ 0 \end{pmatrix} = \underline{q}^{(i)T} = \underline{s}^T$$

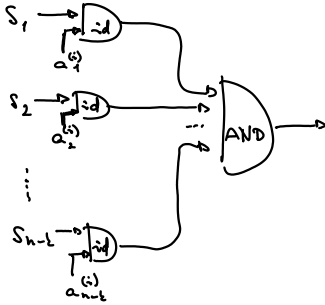
↑  
column vector consists of only zeros

$$\dim(\underline{q}^{(i)}) = n - k = \forall i = 1, \dots, n$$

if they are equal, then we can identify "i"  
 ⇒ so we can tell that there is an error on the bit in the "i"-th position



Matcher circuit can be designed simply:



Criteria:

- $p_i^{(i)} \neq 0 \quad \forall i = 1 \dots n$
- $p_i^{(i)} \neq p_j^{(j)} \quad \forall i, j = 1 \dots n, i \neq j$

Length of code:

$$\left. \begin{aligned} C(n, l) \\ 2^{n-l} - 1 = n \\ 2^{n-l} = n + 1 \\ \sum_{i=0}^{n-l} \binom{n-l}{i} = 2^{n-l} \end{aligned} \right\} \text{for single-bit errors}$$

General form:  $\sum_{i=0}^{n-l} \binom{n-l}{i} = 2^{n-l}$  where  $l$  is the number of corrected bits

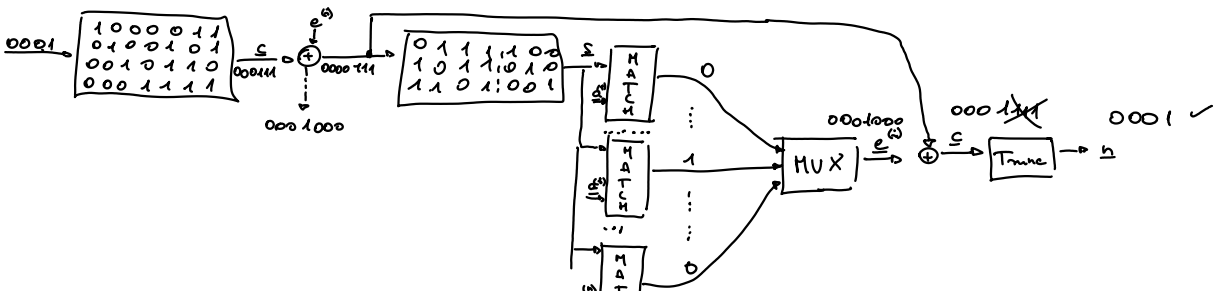
n	l
3	1
7	4
15	11
31	26

$$C(7, 4) \Rightarrow G_{4 \times 7} = \begin{pmatrix} 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \\ 0 & 0 & 1 & 1 & 1 & 1 & 1 \end{pmatrix}$$

$\downarrow \quad \downarrow \quad \downarrow \quad \downarrow$   
 $3 \ 5 \ 6 \ 7$

columns of parity check matrices are organized so that the decimal code is increasing

Example



Why do we love it so much?

$$\frac{a-1}{2}$$

$$\frac{a_{\min}-1}{2} = 1$$

$$d_{\min} \geq 3$$

$$d_{\min} \leq k-l+1 = 3-1+1$$

}  $d_{\min} = 3 = n-l+1 = 0$  "This is my first MDS code!"